Fig. 1

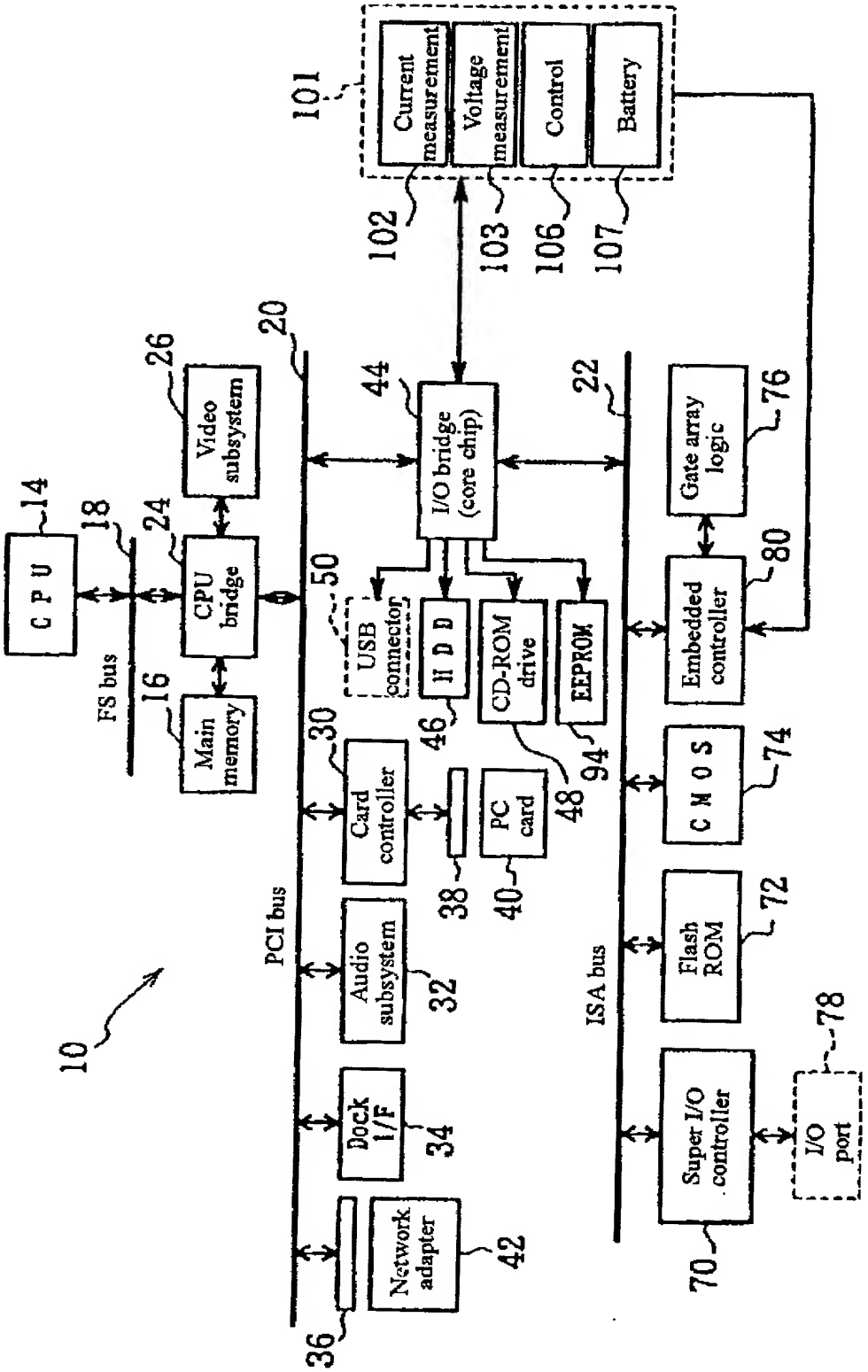

10
CPU
14
18
FS bus
16
Main memory
CPU bridge
24
Video subsystem
26
20
PCI bus
Card controller
30
PC card
38
40
Audio subsystem
32
Dock I/F
34
Network adapter
42
36
50
USB connector
I/O bridge (core chip)
44
HDD
46
CD-ROM drive
48
EEPROM
94
22
ISA bus
Gate array logic
76
Embedded controller
80
CMOS
74
Flash ROM
72
Super I/O controller
70
I/O port
78
101
Current measurement
Voltage measurement
Control
Battery
102
103
106
107

Fig. 2

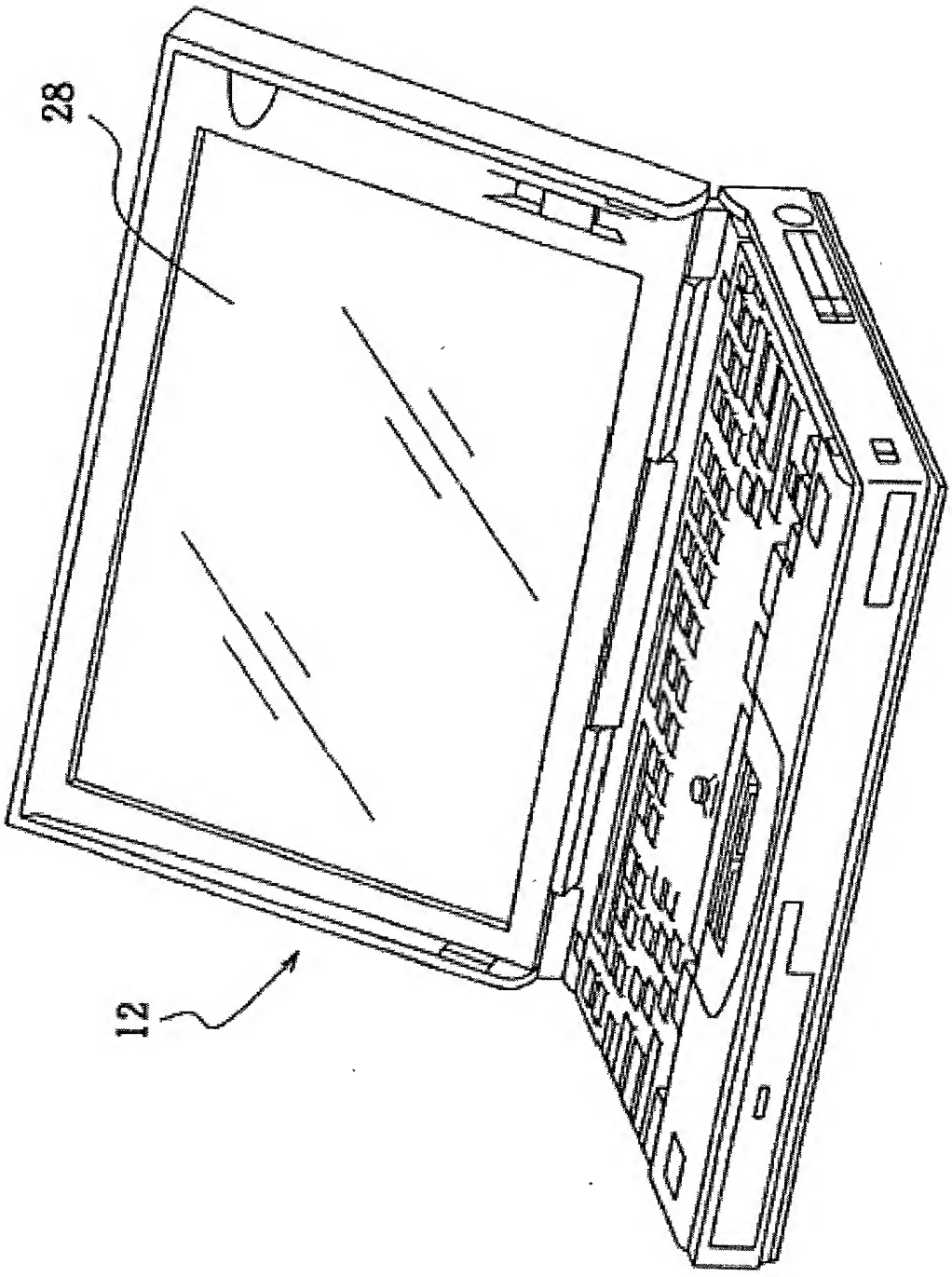
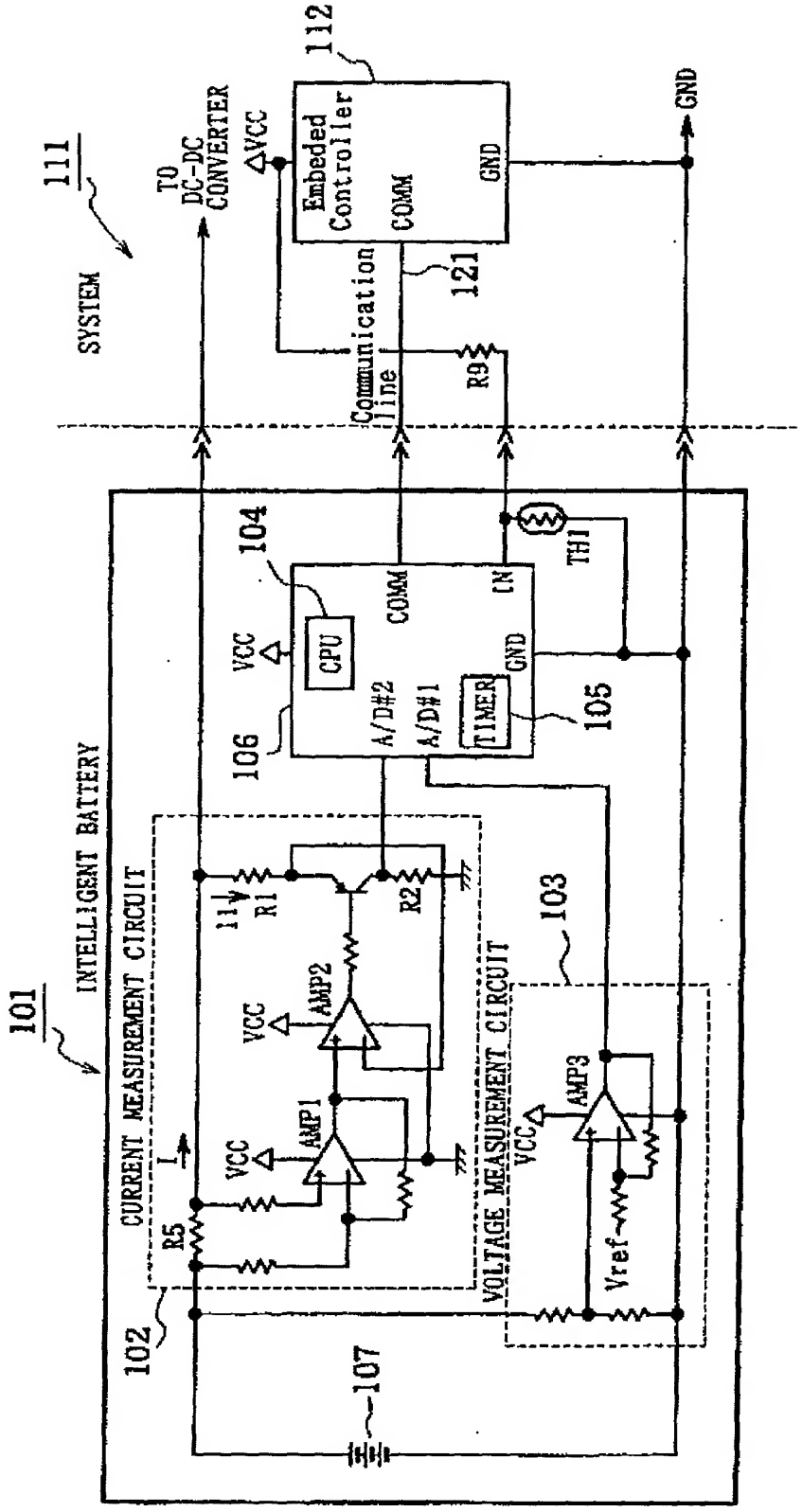


Fig. 3


111
SYSTEM
TO DC-DC CONVERTER
VCC
112
Embeded Controller
COMM
GND
GND
121
Communication line
R9
TH1
104
CPU
VCC
COMM
IN
GND
A/D#2
A/D#1
TIMER
105
106
INTELLIGENT BATTERY
101
CURRENT MEASUREMENT CIRCUIT
I1
R1
R2
103
AMP2
AMP1
AMP3
VCC
I
R5
VOLTAGE MEASUREMENT CIRCUIT
Vref
102
107

PROTECTED DOCUMENT

Bit	Mode	Description
Bits 15 to 14	Power mode	00: Normal operational mode 01: N/A 10: Reconnection 11: Soft-off mode
Bits 13 to 0	Consumption electric current value	Input: Positive integer Unit: 0.1 mA Range: 0 mA to 1638.3 mA

Fig. 4